L	Hits	Search Text	DB	Time stamp
Number				
1	3	PERROTT-MICHAEL-H.IN.	USPAT	2003/10/01
				09:07
2	3	PERROTT-MICHAEL-H.IN.OR	USPAT	2003/10/01
1	1	SILICON-LABORATORIES.AS.		09:07
3	3	PERROTT-MICHAEL-H.IN. OR	USPAT	2003/10/01
		SILICON-LABORATORIES.AS.		09:08
4	0	SILICON-LABORATORIES.AS.	USPAT	2003/10/01
				09:08
6	68	SILICON ADJ LABORATORIES.AS.	USPAT	2003/10/01
				09:11
5	334429	SILICON AQDJ LABORATORIES.AS.	USPAT	2003/10/01
				09:11
7	15	BIT ADJ ERROR ADJ RATE ADJ DETECTION	USPAT	2003/10/01
	•		•	09:50
8	4	BIT ADJ ERROR ADJ RATE ADJ DETECTION AND	USPAT;	2003/10/01
		DATA ADJ STREAM	US-PGPUB;	09:49
			EPO; JPO	
9	34	BIT ADJ ERROR ADJ RATE ADJ DETECTION	USPAT;	2003/10/01
			US-PGPUB;	09:50
L			EPO; JPO	

	Current XRef	Retrieval Classif	Inventor	ß	บ	C.	7	ю	4	5	Image Doc. Displayed	PT
Н	327/159; 327/2		Perrott, Michael H.	☒							US 6590426	
2	327/12; 327/147; 327/156		Perrott, Michael H.	☒							US 6580376	
3	331/16; 331/23; 375/303		Perrott, Michael H. et al.	☒							US 6008703	
4	375/141; 375/362		Ohgoshi, Yasuo et al.								US 5943329	
. 5	714/700; 714/814		Dalmia, Kamal et al.								US 5835501	
9	714/704		Bullock, Dean C. et al.								US 5764651	
7	714/704		Chang, Hyokang								US 5550837	
ω	714/705; 714/713; 714/715; 714/762		Pregont, Gary J. et al.							<u> </u>	US 5351245	
6	714/708		Lada, Timothy P.								US 5305323	
10	714/812		Furlong, Darrell								US 5099500	
11	455/277.1; 714/704		Freeburg, Thomas A.							<u> </u>	US 5036515	

	Þ	П	Δ	Document ID	Issue Date	Pages	Title	Current OR
12	⊠	⊠	us	4100531 A	19780711	7	Bit error rate measurement above and below bit rate tracking threshold	375/225
13	⊠	⊠	ΩS	591383 B	20030708	12	Bit error rate detection	714/704
14	☒	☒	JP A	2000333246	20001130	7	RADIO COMMUNICATION SYSTEM	
15	⊠	☒	J P A	2000269884	20000929	7	RADIO COMMUNICATION SYSTEM	
16	☒	☒	JP A	2000068874	20000303	80	RADIO RECEIVER AND ITS INTERMITTENT RECEPTION METHOD	
17	Ø	\boxtimes	JP	61	19980106	6		
18	☒	⊠	JP	09331309 A	19971222	6	SEQUENTIAL DECODER	
19	⊠	☒	JP	09074578 A	19970318	4	RADIO CALL RECEIVER WITH BIT ERROR RATE DETECTION FUNCTION	
20		\boxtimes	JP	08330992 A	19961213	10	ERROR RATE DETECTION CIRCUIT FOR FM MULTIPLEX BROADCASTING RECEIVER	
21	Ø	\boxtimes	•	06062067 A	19940304	10	DELAY DETECTION CIRCUIT	
22	☒	☒	JP	02055435 A	19900223	വ	COMMON INTEGRATION METHOD	
23	Ø	\boxtimes	五万	400983 A2	19901205	Ŋ	Bit error rate detection.	
24	☒	☒	ВР	980161 Al	20000216	19	DIGITAL DATA RECEIVER	
25		⊠	MO	2079988 A1	20021010	22	METHODS AND APPARATUS FOR BURST TOLERAN EXCESSIVE BIT ERROR RATE ALARM DETECTION AND CLEARING	

	Current XRef	Retrieval Classif	Inventor	ß	ບ	Д	7	е	4	72	Image Doc. Displayed	PT
12			Fletcher, James C. Administrator of the National Aeronautics and Space et al.								US 4100531	
13	714/802		Michel, David et al.								US 6591383	
14			NUMAGAMI, KOICHI							7	JP 2000333246 A	
15			NUMAGAMI, KOICHI							, ~	JP 2000269884 A	
16			YOSHIDA, KOTARO							, r.	JP 2000068874 A	
17		0	YAGI, TOSHIHARU								JP 10004361 A	
18			YAGI, TOSHIHARU								JP 09331309 A	
19			TAKAOKA, YASUHARU								JP 09074578 A	
20			KIMURA, KAZUHIRO et al.							, ,	JP 08330992 A	
21	375/329		: -								06062067	
	714/795		KURODA, YUJI								JP 02055435 A	
23	455/277.2; 714/704		FREEBURG, THOMAS A								EP 400983 A2	
24			HASHIMOTO, AKINORI et al.								EP 980161 A1	
25			SOLTYSIAK, EDWARD et al.								WO 2079988 A1	